

REMARKS

Claims 1-13 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections are respectfully requested in light of the following remarks.

Turning now to the art rejections, Claims 1-13 were rejected under 35 U.S.C. § 103 as being unpatentable over Szepesi.

It is respectfully submitted that Szepesi does not disclose or suggest the presently claimed invention including the capacitor directly coupled across the drains of the first FET and the second FET.

The Examiner alleges that it would have been obvious to one of ordinary skill in the art to eliminate the diode and low pass filter.

Applicants traverse.

The elements in question could not be removed without changing the operation of Szepesi's circuit.

Consequently, the removal of these specific elements are not obvious.

Additionally, Applicants submit that the Examiner has engaged in impressive hindsight in the modification of the Szepesi reference in order to reject Applicant's claims.

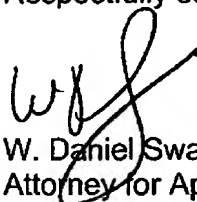
Reconsideration is requested.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant amendment places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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